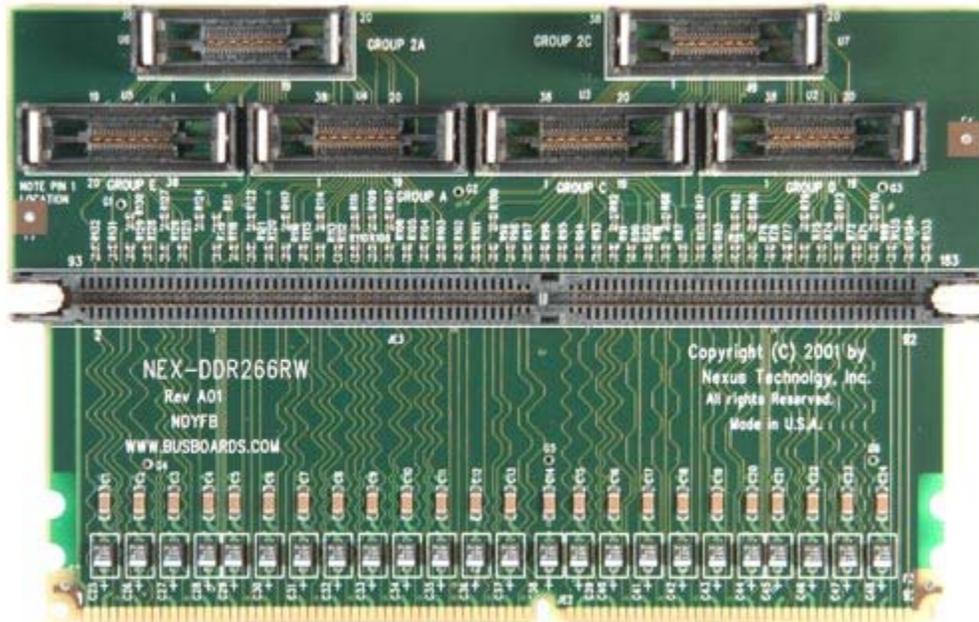

NEX-DDR266RW



- Acquisition of Read and Write data from DDR200 and DDR266
- Quick and easy connection between the DDR bus and a Tektronix Logic Analyzer
- Extender design does not require a dedicated slot
- Supports 184-pin, 2.5V, Unbuffered or Registered DDR SDRAM DIMMs up to 266MHz
- Impedance controlled
- Matched trace lengths
- <55ps skew between any two channels
- No active buffering of the DDR signals
- Accurate 2-8GHz timing analysis (depending on acquisition module(s) used)
- Simultaneous state and timing on every channel of the TLA
- Trigger on setup/hold violations
- Correction with data from other acquisition modules
- Several GND points close to the DDR socket for use with a scope probe
- Use the TLA's Extended iView capabilities to view any channel on an oscilloscope without re-probing (depending on the acquisition module(s) used)
- AMP Mictor Connector part number is 767054-1

General Description

The NEX-DDR266RW adapter provides clocking, setup, symbolic display of information, and a quick convenient connection to a 2.5 V, 184-pin SDRAM DIMM bus. It is designed as an extender card for the DDR DIMM under test. The socket for the DDR module under test is elevated above adjacent DDR modules for mechanical clearance. Trigger on a Read, Write, Burst, Mode Register Set, etc. for easy problem identification.

2-8Ghz Timing Analysis available for all DDRII signals

Oscilloscope Connectivity on any channel without re-probing via the TLA's Enhanced iView Analog Mux capability (depending on acquisition module(s) used)

No Dedicated Slot Required – The logic analyzer connects above the normal DIMM height so that there is no interference with adjacent DIMMs.

Pre-Defined Symbols for the following Command Cycles allow for easy Trigger Setup:

- Read Col Address Read
- Mode Register Set
- Row Address Strobe
- Ignore Command Data
- Refresh
- Write Col Address Write
- Precharge
- No Op
- Burst Stop
- Precharge Select Bank

Correlating Bus Activity While the NEX-DDR package is being used to monitor the DDR bus activity, another acquisition module can be used to monitor activity elsewhere in the system. The results of the two acquisitions can be correlated in time to determine the sequence of actions that occurred. For instance, the system microprocessor could be monitored and correlated with bus activity to verify the proper response to an external interrupt condition.

The following support package(s) are included with this product:

DDR200 offers the ability to synchronously acquire 200MHz DDR Address and Command signals on every edge of DDR CK0 or CK1. DDR200 read data **or** write data can also be acquired – the acquisition of DDR data requires a valid data window of approximately 1.6ns. This support requires one 136-channel acquisition card (or TLA6X4) with the 200MHz state clocking option.

DDR200M requires two merged 136-channel acquisitions cards and is designed to give the user the ability to acquire both Read and Write data at a 200MHz data rate. For this support to work, both merged cards must have the 200MHz state clocking option. The DDR200M support is usable with a TLA7XX-series Logic Analyzer only as two acquisition cards are required for this support.

DDR266 also requires two merged 136-channel acquisition cards. This support is designed to give the user the ability to acquire either DDR266 Read or Write data. For this support to work, both merged cards must have the 200MHz state clocking option. The DDR266 support is usable with a TLA7XX-series Logic Analyzer only as two acquisition cards are required for this support.

DDR266M requires three merged 136-channel acquisition cards. This support is designed to give the user the ability to acquire both DDR266 Read and Write data. For this support to work, all merged cards must have the 200MHz state clocking option. The DDR266RW support is usable with a TLA7XX-series Logic Analyzer only as three acquisition cards are required for this support. The TLA must also be running V3.2 or later of the TLA Application Software.

LA Support / Configuration

Acquisition Type	TLA5204	TLA6x4	TLA7L/M/N/P/Q4 Module(s)	TLA7AA/AB4 Module(s)
DDR200/266 Timing Only	136-channel - 4 P6434 probes	136-channel - 4 P6434 probes	1 module - 136-channel - 4 P6434 probes	1 module - 136-channel - 4 P6860 probes - 4 Mictor adapters
DDR200 Read or Write Data	136-channel - 4 P6434 probes	136-channel - 200MHz option - 4 P6434 probes	1 module - 200MHz option - 136-channel - 4 P6434 probes	1 Module - 136-channel - 235MHz option - 4 P6860 probes - 4 Mictor adapters
DDR200 Read and Write Data	NA	NA	2 merged modules - 200MHz option - 136-channel - 4 P6434 probes	2 merged modules - 136-channel - 235MHz option - 4 P6860 probes - 4 Mictor adapters
DDR266 Read or Write Data	NA	NA	2 merged modules - 200MHz option - 136-channel - 4 P6434 probes	2 merged modules - 136-channel - 235MHz option - 4 P6860 probes - 4 Mictor adapters
DDR266 Read and Write Data	NA	NA	3 merged modules - 200MHz option - 136-channel - 6 P6434 probes	3 merged modules - 136-channel - 235MHz option - 6 P6860 probes - 6 Mictor adapters

** Read **and/or** Write at greater than 266MHz not available with this product. Please see NEX-DDRHS product for Read **and/or** Write support at speeds greater than 266MHz.

Ordering / Contact Information

Part Number NEX-DDR266RW

Includes: NEX-DDR266RW adapter
DDR200, DDR200M, DDR266 and DDR266M Support Software
Manual

Additional software includes

NEX-SPA - TLA Software Plug-In for determining optimum Setup & Hold sample points.

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Placing an Order

Credit Card orders can be placed directly at 877-595-8116.

Purchase orders can be faxed to 877-595-8118.

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