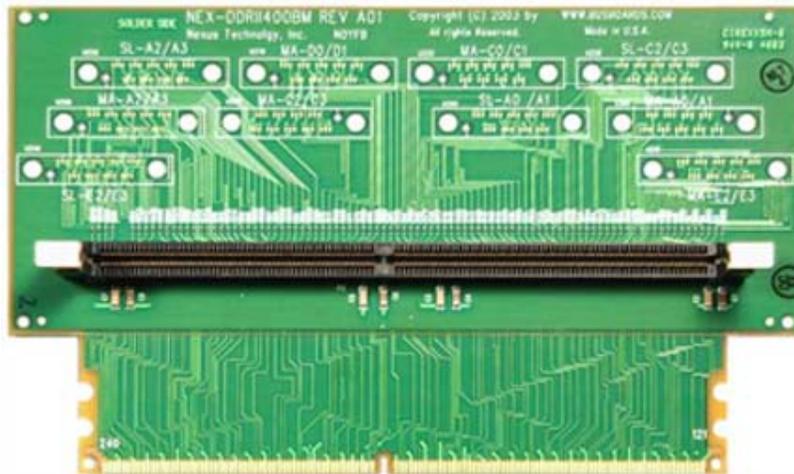

NEX-DDRII400BM



- Acquisition of DDRII 400MT/s Address/Command, Read and Write Data
- Mirror image of NEX-DDRII400 that enables simultaneous monitoring of two, adjacent DDRII DIMM slots
- Quick and easy connection between the DDRII bus and a Tektronix Logic Analyzer
- Extender design does not require a dedicated slot
- Simultaneous acquisition of DDRII Read and Write data
- Selective Clocking provides better utilization of logic analyzer memory by reducing acquisition of Refresh and Idle bus cycles
- Supports 240-pin, unbuffered or registered DDRII SDRAM DIMMs up to 400 MT/s
- Impedance controlled
- Matched trace length design
- No active buffering of the DDR signals
- Accurate 8GHz timing analysis on every channel of the TLA
- Simultaneous state and timing on every channel of the TLA
- Correlate DDRII data with data from other acquisition modules
- Use the TLA's Extended iView capabilities to view any channel on an oscilloscope without re-probing
- Software only support is available for embedded memory designs

General Description

NEX-DDR2400BM allows for the acquisition of Address/Command, Read and Write Data of 240-pin, unbuffered or registered DDR2 SDRAM DIMMs up to 400 MT/s

8Ghz Timing Analysis available for all DDR2 signals

Oscilloscope Connectivity on any channel without re-probing via the TLA's Enhanced iView Analog Mux capability

Selective Clocking stores data when commands are present and for 13 clock cycles after Column Address Assertion. This results in fewer Idle cycles being stored in acquisition memory.

Pre-Defined Symbols for the following Command Cycles allow for easy Trigger Setup:

- Read Col Address Read
- Write Col Address Write
- Mode Register Set
- Row Address Strobe
- Precharge
- Ignore Command Data
- Burst Stop
- Refresh
- Precharge Select Bank
- No Operation

No Dedicated Slot Required – The logic analyzer connects above the normal DIMM height so that there is no interference with adjacent DIMMs.

The following support package(s) are included with this product:

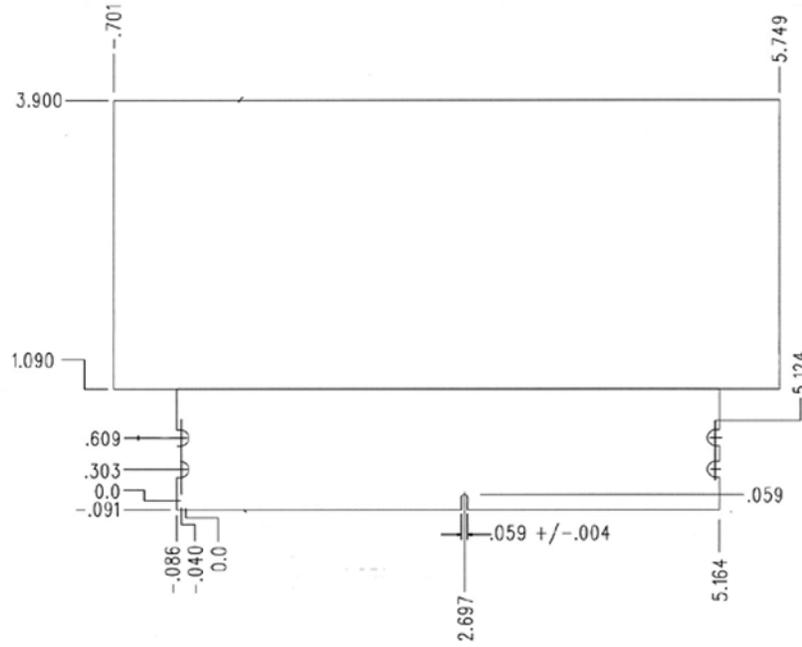
MDDR2 allows the user to acquire both Read AND Write DDR2 data from a target. This support requires 8-P6860 High-Density Compression probes.

LA Support / Configuration

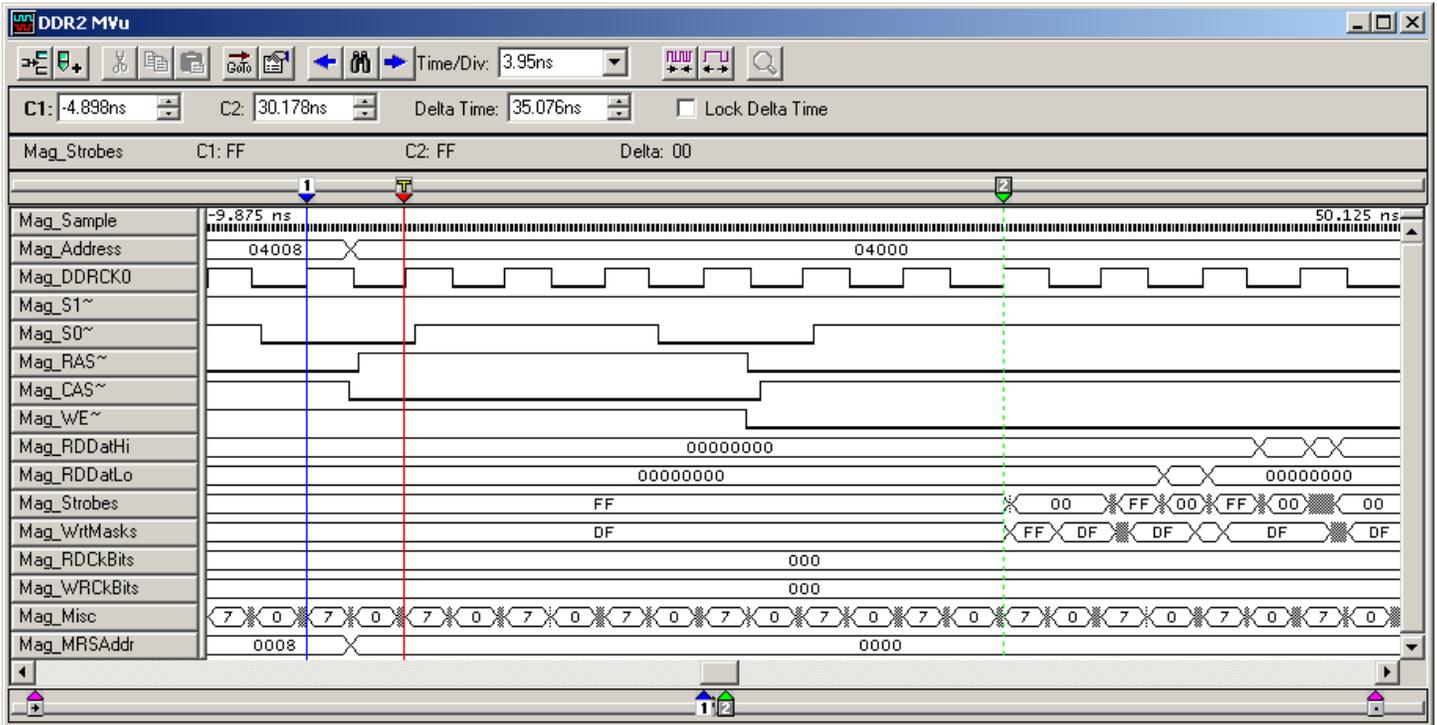
A TLA700 equipped with two, merged, 450MHz state speed acquisition modules (TLA7AA4 or TLA7AB4 cards) are required. When the upper Strobe/Write Mask signals are used as differential strobes (400DDR2S support) 1-P6880 and 7-P6860 probes are required. When the upper Strobe/Write Mask signals are used as Write Masks or single-ended strobes (400DDR2M support) 8-P6860 probes are required.

8GHZ MagniVu Timing and Enhanced iView Analog Mux capabilities are available with either configuration.

Mechanical Outline



Timing Display



State Display

Sample	DDR2_400 Address	DDR2_400 RDDatHi	DDR2_400 RDDatLo	DDR2_400 WRDatHi	DDR2_400 WRDatLo	DDR2_400 Mnemonics	DDR2_400 Strokes	DDR2_400 WrtMasks	Timestamp
737	-----	00000000	00000000	-----	-----	READ DATA	FF	DF	12.375 ns
738	04002	-----	-----	-----	-----	READ - COL ADDR READ (S0~)	FF	DF	2.625 ns
739	-----	00000000	00000000	-----	-----	READ DATA	FF	DF	2.250 ns
740	04000	-----	-----	-----	-----	PRE - PRECHARGE SELECT BANK (S0~)	FF	DF	2.375 ns
743	-----	00000000	00000000	-----	-----	READ DATA	FF	DF	2.625 ns
744	-----	00000000	00000000	-----	-----	READ DATA	FF	DF	7.375 ns
745	-----	00000000	00000000	-----	-----	READ DATA	FF	DF	2.750 ns
746	-----	00000000	00000000	-----	-----	READ DATA	00	FF	2.250 ns
766	04400	-----	-----	-----	-----	PRE - PRECHARGE SELECT BANK (S0~)	00	DF	2.625 ns
767	04008	-----	-----	-----	-----	ACTV - ROW ADDRESS STROBE (S0~)	FF	DF	5.325,875 us
768	04002	-----	-----	-----	-----	READ - COL ADDR READ (S0~)	FF	DF	709.829,875 us
773	-----	00000000	00000000	-----	-----	READ DATA	FF	DF	5.000 ns
774	04002	-----	-----	-----	-----	READ - COL ADDR READ (S0~)	FF	DF	12.375 ns
775	-----	00000000	00000000	-----	-----	READ DATA	FF	DF	2.625 ns
776	04000	-----	-----	-----	-----	PRE - PRECHARGE SELECT BANK (S0~)	FF	DF	2.375 ns
779	-----	00000000	00000000	-----	-----	READ DATA	FF	DF	2.750 ns
780	-----	00000000	00000000	-----	-----	READ DATA	FF	DF	7.250 ns
781	-----	00000000	00000000	-----	-----	READ DATA	00	FF	2.750 ns
782	-----	00000000	00000000	-----	-----	READ DATA	00	DF	2.250 ns
802	04400	-----	-----	-----	-----	PRE - PRECHARGE SELECT BANK (S0~)	00	DF	2.625 ns
803	04008	-----	-----	-----	-----	ACTV - ROW ADDRESS STROBE (S0~)	FF	DF	19.331,875 us
804	04002	-----	-----	-----	-----	READ - COL ADDR READ (S0~)	FF	DF	695.873,250 us
809	-----	00000000	00000000	-----	-----	READ DATA	FF	DF	4.875 ns
810	04003	-----	-----	-----	-----	READ - COL ADDR READ (S0~)	FF	DF	12.375 ns
811	-----	00000000	00000000	-----	-----	READ DATA	FF	DF	2.750 ns
812	04000	-----	-----	-----	-----	PRE - PRECHARGE SELECT BANK (S0~)	FF	DF	2.250 ns
815	-----	00000000	00000000	-----	-----	READ DATA	FF	DF	2.750 ns
816	-----	00000000	00000000	-----	-----	READ DATA	FF	DF	7.375 ns
817	-----	00000000	00000000	-----	-----	READ DATA	FF	DF	2.625 ns
818	-----	00000000	00000000	-----	-----	READ DATA	00	FF	2.250 ns
838	04400	-----	-----	-----	-----	PRE - PRECHARGE SELECT BANK (S0~)	00	DF	2.750 ns
839	04008	-----	-----	-----	-----	ACTV - ROW ADDRESS STROBE (S0~)	FF	DF	12.454,625 us
840	04002	-----	-----	-----	-----	READ - COL ADDR READ (S0~)	FF	DF	702.753,000 us
845	-----	00000000	00000000	-----	-----	READ DATA	FF	DF	5.000 ns
846	04003	-----	-----	-----	-----	READ - COL ADDR READ (S0~)	FF	DF	12.375 ns
847	-----	00000000	00000000	-----	-----	READ DATA	FF	DF	2.625 ns
848	04000	-----	-----	-----	-----	PRE - PRECHARGE SELECT BANK (S0~)	FF	DF	2.375 ns
							FF	DF	2.625 ns

Software Mode, Address, Data and Command Cycles Only

Ordering / Contact Information

Part Number NEX-DDR2400BM

Includes: NEX- DDR2400BM adapter
Software
Manual

Additional software included:

NEX-DDRSPA - TLA Software Plug-In for determining optimum Setup & Hold sample points.

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Placing an Order

Credit Card orders can be placed directly at 877-595-8116.

Purchase orders can be faxed to 877-595-8118.

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