

# DDR4 Interposers

DIMM | Dynamic Termination | Protocol/Compliance

## OVERVIEW

The DDR4-D-DIM-288 interposer enables probing of DDR4 DIMM (UDIMM, RDIMM or LRDIMM) with an MA4100 series analyzer.

- Passive 288-pin slot interposer
- Unbuffered, Registered or Load-Reduced DIMMs (UDIMM, RDIMM or LRDIMM)
- JEDEC Standard JESD49-4
- Address, command and control probing with a MA4100 series analyzer



## ANALYZERS AND PROBES

A MA41x0 series logic analyzer / real-time compliance analyzer and a PRB-XL-1 probe is required to enable protocol debug, compliance analysis and oscilloscope cross-triggering. With a turn-key setup, the MA4100 can analyze thousands of real-time memory parameters across clock stops and frequency changes. Also includes 11ps x 10mV x 20-channel analog characterization (iCiS™) and dynamic probe termination.



## TESTING SERVICES

The following testing services are available for this interposer.

### TARGET BOOT & BASIC FUNCTIONALITY

For each target, this service will power on and ensure functionality of the target with the interposer(s) installed.

### MA4100: SIGNAL ANALYSIS & BASIC COMMAND ACQUISITION

This service will power-on the target and place the target in a predefined state. A MA4150 analyzer will then be connected to the target and the following tests will be performed and the following reports will be provided.

- iCiS eye analysis report of all command, address, and control signals, including recommended sampling positions and a MA4100 System Setup file.
- A real-time JEDEC protocol report, including pass/fail for all basic protocol/sequence checks. Any failures will be noted in the report and an Max100 Saved Session file will be provided so that the customer may review the results.

## MA4100: MARGIN TESTING – ONE FREQUENCY

This service will power on the target and place the target in a predefined state. A MA4100 analyzer will be used to perform the following tests and the following reports will be provided.

- Real-time MRS/MRW capture of configuration information.
- A real-time JEDEC margin test of one frequency. Any failures will be noted in the report and an MA4100 Saved Session file will be provided so that the customer may review the results.

## MA4100: ADDITIONAL / CUSTOM SERVICES

Additional and custom services may also be requested.

## DIMENSION DRAWINGS

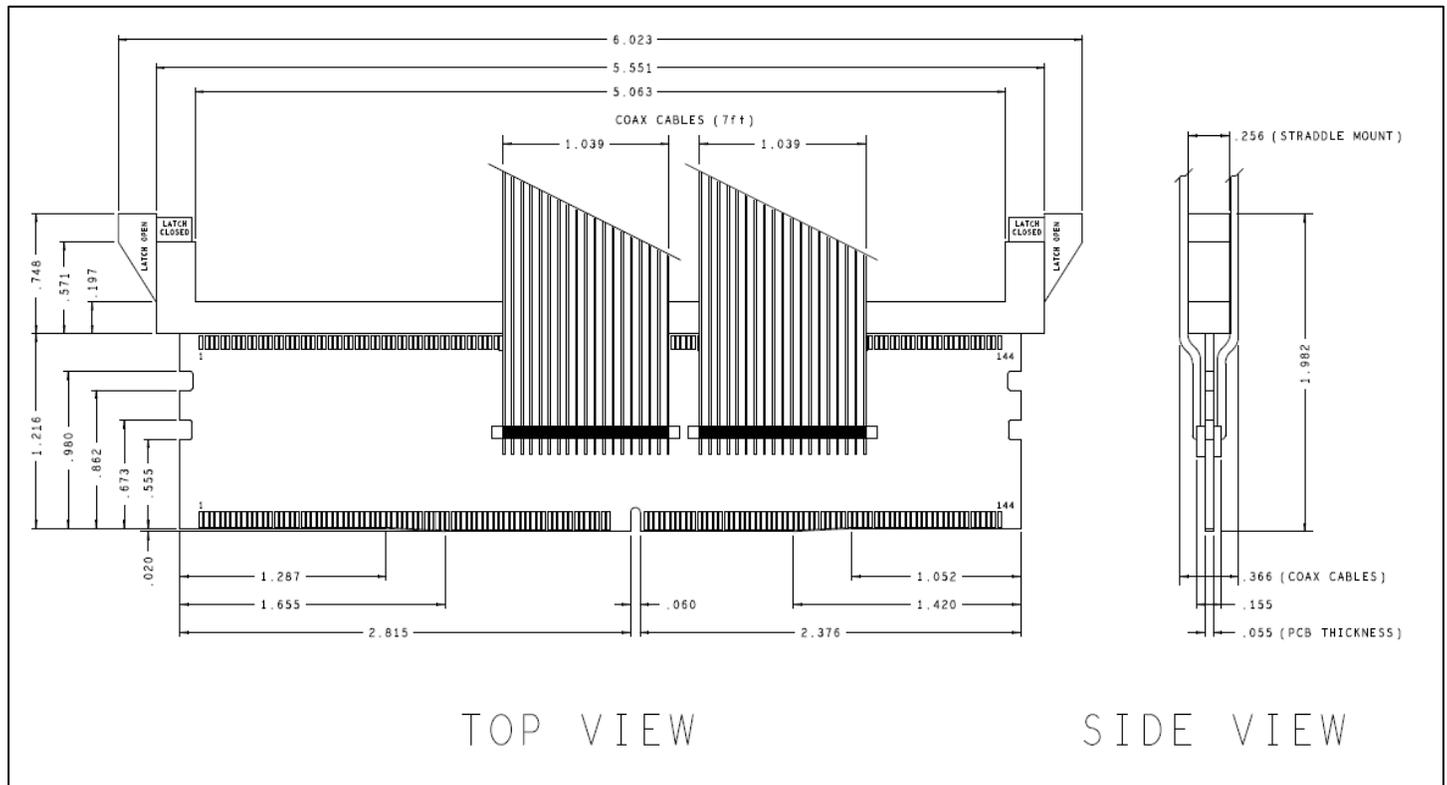


Figure 1 DD4-D-DIM-288 Interposer Dimensions

## SIGNALS PROBED (ALL DIMM TYPES)

- CKE0
- CKE1
- CS0#
- CS1#
- CS2#
- CS3#
- BA0
- BA1
- BG0
- BG1
- A00
- A01
- A02
- A03
- A04
- A05
- A06
- A07
- A08
- A09
- A10/AP
- A11
- A12/BC#
- A13
- A14/WE#
- A15/CAS#
- A16/RAS#
- A17
- C0
- C1
- C2
- ACT#
- ALERT#
- PAR
- RESET# (optional)

## PRODUCT CONFIGURATION TABLE

Product Name	Description	Nexus Order Number
DDR4-D-DIM-288	DDR4 DIMM Slot Interposer	NEX-DDR4INTR288-CMPL



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